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# 15/003  
4/15/02  
Harrison

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/388,857  
Filing Date ..... September 1, 1999  
Inventor ..... Luan C. Tran  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... L. Schillinger  
Attorney's Docket No. .... MI22-878  
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor  
Processing Methods Of Forming Dynamic Random Access Memory Circuitry, And  
Related Integrated Circuitry

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to  
the references listed on the attached Form PTO-1449. No admission is made regarding  
whether the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: Apr 23, 2002



Frederick M. Fliegel, Ph.D.  
Reg. No. 36,138  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828  
(509) 624-4276

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